

IN THE CLAIMS

1. (Currently Amended) A method for execution in an electronic device, comprising:

detecting in ~~an~~said electronic device a bus width or widths available for use in ~~an operating mode of the~~a peripheral device connected to the electronic device by detecting one or more indirect indicators formed in the peripheral device, ~~which~~said one or more indirect indicators ~~is itself or are themselves~~being only indirectly indicative of ~~which one or ones of~~said bus width or widths available for use in the peripheral device ~~in said operating mode~~, said electronic device then

selecting a detached bus width from said bus width or widths detected as available for ~~using~~use in said peripheral device ~~in said operating mode according to the selected bus width~~.

2. (Previously Presented) The method according to claim 1, wherein reference data is stored in the electronic device about at least one bus width available in the peripheral device and corresponding to one or more values of said one or more indirect indicators.

3. (Previously Presented) The method according to claim 2, wherein said one or more indirect indicators is or are indicative of information stored in the peripheral device and indicating indirectly, said bus width or widths available in the peripheral device.

4. (Previously Presented) The method according to claim 3, wherein said indirect indicator or indicators is or are indicative of information about a clock frequency available in the peripheral device.

5. (Previously Presented) The method according to claim 3, wherein said information is information about whether the peripheral device is fast or slow.

6. (Previously Presented) The method according to claim 3, wherein said indicator or indicators is or are indicative of a version of the peripheral device.

7. (Currently Amended) The method according to claim 2, comprising performing at least the following:

transmitting a request from the electronic device to the peripheral device to transmit in return said one or more values of said indirect indicator or indicators to the electronic device,

transmitting said value or values of said indirect indicator or indicators from the peripheral device to the electronic device,

comparing said one or more values with at least one reference value from said reference data stored in the electronic device for determining the bus width or widths available for use in the peripheral device,

selecting said one bus width available in the peripheral device according to said identification, and

setting the selected bus width for the peripheral device.

8. (Currently Amended) The method according to claim ~~1~~2, wherein at least one connection line is formed between the electronic device and the peripheral device, and using said at least one said connection line as said indicator.

9. (Currently Amended) The method according to claim 8, comprising performing at least the following:

~~an initialization in~~initializing said peripheral device, ~~in which~~wherein a value of said at least one connection line is set to correspond indirectly to the bus width or widths available in the peripheral device, wherein said detecting comprises

~~a detection, in which~~ the electronic device ~~examines~~detecting a state of said at least one connection line and ~~compares~~comparing the state of said connection line with at least one reference value from said reference data stored in the electronic device, followed by said

~~a selection for selecting one bus width available in the peripheral device, and~~  
then

~~a setting for setting the selected bus width for~~ said using the peripheral device in said operating mode.

10. (Currently Amended) A system comprising an electronic device, a peripheral device which can be connected to the electronic device and in which at least one bus width is arranged to be used from a defined set of bus widths, and which system comprises a bus width detector for detecting at least one bus width available in a selected mode of the peripheral device connected to the electronic device, wherein the peripheral device is provided with one or more indirect indicators, ~~which one or~~

~~more indirect indicators is itself or are themselves~~ being only indirectly indicative of ~~which one or ones from said set of bus width or widths are~~ available for operating the peripheral device in said selected mode.

11. (Currently Amended) An electronic device comprising a bus width detector for detecting the bus width or widths available in a selected mode of a peripheral device connected to the electronic device, in which peripheral device at least one bus width is arranged to be used from a defined set of bus widths ~~on~~in said selected mode, the bus width detector also comprising a control unit for determining ~~the value of one or~~ more indirect ~~indicators~~indicator values formed in the peripheral device, ~~which said~~ one or more indirect ~~indicators is itself or themselves~~indicator values being only indirectly indicative of which ~~one or ones~~bus width or bus widths of said set of bus widths is or are available for operating the peripheral device in said selected mode.

12. (Currently Amended) The electronic device according to claim 11, wherein reference data is stored in the electronic device about at least one bus width available in the peripheral device ~~and corresponding to~~for comparing said reference data to said one or more indirect indicator valuevalues for determining said bus width or widths available in said selected mode.

13. (Currently Amended) The electronic device according to claim 12, wherein said indirect indicator arranged to be used is information stored in the peripheral device and indicating indirectly, which ~~one or ones of said set of~~bus width or bus widths are available in the peripheral device.

14. (Currently Amended) The electronic device according to claim 13, wherein at least one connection line is formed between the electronic device and the peripheral device, and ~~that~~with said one or more indirect ~~indicator~~indicators arranged to be used ~~is~~comprises said at least one said connection line.

15. (Currently Amended) The electronic device according to claim 14, said bus width detector comprising means for ~~examining a value of~~detecting said connection line.

16. (Currently Amended) A peripheral device which can be connected to an electronic device comprising a bus width detector for detecting ~~the~~a bus width of the peripheral device connected to the electronic device, and in which peripheral device at least one bus width from a defined set of bus widths is arranged to be used in a selected mode, wherein the peripheral device is provided with one or more indirect indicators ~~which is itself or are themselves~~being only indirectly indicative of ~~which one or ones of~~ said at least one bus width from said defined set of bus widths is or are available for operating the peripheral device in said selected mode.

17. (Currently Amended) The peripheral device according to claim 16, wherein information about ~~the~~a maximum clock frequency available in the peripheral device is stored in a memory of the peripheral device.

18. (Previously Presented) The peripheral device according to claim 16, wherein at least a fast peripheral device and a slow peripheral device have been defined, wherein information about whether the peripheral device is fast or slow is stored in a memory of the peripheral device.

19. (Previously Presented) The peripheral device according to claim 16, wherein information about version of the peripheral device is stored in a memory of the peripheral device.

20. (Previously Presented) The peripheral device according to claim 16, comprising at least one connection line, and a control unit for setting said connection line in a value which indirectly corresponds to the bus widths available in the peripheral device.

21. (Currently Amended) A memory card comprising a bus width detector for detecting a bus width of the memory card connected to the electronic device, and in which memory card at least one bus width from a defined set of bus widths is arranged to be used in a selected mode of the memory card, wherein the memory card is provided with one or more indirect indicators ~~which is itself or are themselves~~being only indirectly indicative of ~~which one or ones of said set of bus widths are available for operating the memory card~~at least one bus width arranged to be used in said selected mode.

22. (Currently Amended) An electronic device comprising a bus width detector for detecting a bus width of a peripheral device connected to the electronic device, in which peripheral device at least one bus width is arranged to be used from a defined set of bus widths in a selected mode of the peripheral device, the bus width detector is configured to determine a value of one or more indirect indicators formed in the peripheral device, ~~which said~~ one or more indirect indicators ~~is itself or themselves being~~ only indirectly indicative of which ~~one or ones of said set of bus width or bus widths~~ is or are available for operating the peripheral device in said selected mode.

23. (Currently Amended) The electronic device according to claim 22, wherein reference data is stored in the electronic device about at least one bus width available in the peripheral device and corresponding to said ~~indirect indicator~~ value.

24. (Currently Amended) A peripheral device which can be connected to an electronic device comprising a bus width detector for detecting ~~the a~~ bus width or a plurality of bus widths of the peripheral device connected to the electronic device, ~~and in which peripheral device at least onesaid~~ bus width ~~from a defined set of for one of said plurality of bus widths is arranged to be used for use~~ in a selected mode of the peripheral device, wherein the peripheral device is provided with one or more indirect indicators ~~which is itself or are themselves only~~ indirectly indicative of ~~which one or ones of said set of said bus width or of said one of said plurality of bus widths that are available arranged for operating use in the selected mode of the peripheral device in said selected mode.~~

25. (Currently Amended) The peripheral device according to claim 16, comprising at least one connection line, and a control unit for setting said connection line ~~into~~ a value which indirectly corresponds to the bus width or the one of one plurality of bus widths available in the peripheral device.